



Which NAND Solution is Best for My Design?

Micron offers a full line of high-performance memory solutions—from SLC, MLC, TLC and serial NAND to multichip packages (MCPs/eMCPs), eMMC, eUSB and SSDs—for a variety of applications. And we work with chipset vendors, OS designers, and other enablers to ensure they're optimized for your design.

Technology	Relative Attributes				
	Endurance	ECC Compatibility	Performance	Price/GB	Interface Complexity
Single-Level Cell (SLC)	•••••	•	•••••	\$\$\$\$	••
Serial (SPI) NAND	•••••	•	•••••	\$\$\$\$	•
Enterprise NAND – MLC ¹	•••	•••••	•	\$\$\$	•••••
Multi-Level Cell (MLC)	•••	••	•••	\$\$\$	•••
Triple-Level Cell (TLC)	•	•••	•	\$	•••
Quad-Level Cell (QLC)	•	•••	•	\$	•••
MCPs — NAND with LPDRAM	•••	•	•••	\$\$\$	••
eMMC	••	None	••	\$\$	•
eMCP — eMMC with LPDRAM	••	None	••	\$\$\$	•
Embedded USB (eUSB)	•••••	None ²	•••	\$\$\$\$	•
SSD	•••••	None	•••	\$\$\$\$	•

◀ = Lower; • = Low; •• = Medium; ••• = High; •••• = Higher; ••••• = Highest
 1. Requires enhanced ECC and relaxed timings. 2. ECC circuit and processing built in.

Technology	NAND Suitability by Application										
	Card/USBs ³	Media Players	Cameras	Connected Home	Mobile Handsets	Portable Navigation	Automotive	Enterprise/Industrial	Medical	Networking	STB/DTV
SLC NAND, SPI NAND	•	•	•		•		•	•	•	•	•
MLC, TLC NAND	•	•			•	•		•			
QLC NAND	•	•									
Enterprise NAND								•			•
eMMC		•	•		•	•	•	•	•	•	•
eMCP — eMMC with LPDRAM			•	•	•			•			
MCPs — NAND with LPDRAM			•		•		•	•	•		
Embedded USB (eUSB)								•		•	
SSDs	•	•	•		•	•	•	•	•	•	•

3. Performance-dependent.