

Errata

P33 Parallel NOR Flash Memory

JS28F128P33BF70A

JS28F128P33TF70A

JS28F640P33BF70A

JS28F640P33TF70A

Introduction

This document is an update to the specifications for the Micron® P33 64M/128M Parallel NOR Flash Memory data sheet. It is intended for hardware system manufacturers and software developers of applications, operating systems, and tools.

Scope

This document provides an explanation of device errata, including errata problem statement, implications, workaround, and status.

This document may also contain information that was not previously published.

This document should be used with and may include updates to the specifications contained in the P33_65nm_SBC_64M_128M Flash memory data sheets.

Terminology

Errata: Design defects or errors in a published data sheet. Errata may cause a product's behavior to deviate from published specifications. Hardware and software designed for use with any given stepping must assume that all errata documented for that stepping are present on all devices.

Device Identification

This section details how to identify a device affected by the errata defined in this document.

The Marketing Part Numbers (MPN) that identifies an affected device begin with:

JS28F128P33BF70A

JS28F128P33TF70A

JS28F640P33BF70A

JS28F640P33TF70A



This data sheet contains minimum and maximum limits specified over the power supply and temperature range set forth herein. Although considered final, these specifications are subject to change, as further product development and data characterization sometimes occur.

Summary Table of Changes

Table 1 summarizes known issues in the current silicon revision

Table 1: Errata Summary

| Erratum # | Status ¹ | Issue |
|-----------|---------------------|-----------------------------------|
| 1 | No Fix | tCHTX spec error on the datasheet |
| 2 | No Fix | tCHQX spec error on the datasheet |

Note. Doc = Documentation changes only; Fix = This erratum is intended to be addressed; Fixed = This erratum has already been addressed; No Fix = There are no plans to address this erratum

Errata List

Table 2: Known Issues, Workarounds, and Status

| Erratum # | Information | Description |
|-----------|-------------|--|
| 1 | Problem | tCHTX cannot meet datasheet spec—5ns for TSOP package, which is a datasheet error during datasheet creation. |
| | Implication | The tCHTX min value is 3ns for TSOP package. |
| | Workaround | No work around |
| | Status | It is a datasheet error, no fix plan. |
| 2 | Problem | tCHQX cannot meet datasheet spec—5ns for TSOP package, which is a datasheet error during datasheet creation. |
| | Implication | The tCHQX min value is 3ns for TSOP package. |
| | Workaround | No work around |
| | Status | It is a datasheet error, no fix plan. |



Revision History

Rev. A 08/15

- Initial release



8000 S. Federal Way, P.O. Box 6, Boise, ID 83707-0006, Tel: 208-368-3900
 prodmktg@micron.com www.micron.com Customer Comment Line: 800-932-4992
 Micron, the M logo, and the Micron logo are trademarks of Micron Technology, Inc. All other trademarks are the property of their respective owners.