

How DRAM memory works - Quiz

Reviewed 2025



© 2025 Micron Technology, Inc. All rights reserved. Information, products, and/or specifications are subject to change without notice. All information is provided on an "AS IS" basis without warranties of any kind. Statements regarding products, including statements regarding product features, availability, functionality, or compatibility, are provided for informational purposes only and do not modify the warranty, if any, applicable to any product. Drawings may not be to scale. Micron, the Micron logo, and other Micron trademarks are the property of Micron Technology, Inc. All other trademarks are the property of their respective owners.

Copyright guidelines

By using any content provided by the Micron Educator Hub, you acknowledge that Micron Technology, Inc. (“Micron”) is the sole owner of the content and agree that any use of the content provided by the Micron Educator Hub must comply with applicable laws and require strict compliance with these Guidelines:

1. Credit shall be expressly stated by you to Micron for use of the content, including any portion thereof, as follows:
 - a. “© 2025 Micron Technology, Inc. All Rights Reserved. Used with permission.”
2. You may not use the content in any way or manner other than for educational purposes.
3. You may not modify the content without approval by Micron.
4. You may not use the content in a manner which disparages or is critical of Micron, its employees, or Micron’s products/services.
5. Permission to use the content may be canceled/terminated by Micron at any time upon written notice from Micron to You if You fail to comply with the terms herein.
6. You acknowledge and agree that the content is provided by Micron to You on an “as is” basis without any representations or warranties whatsoever, and that Micron shall have no liability whatsoever arising from Your use of the content. Micron shall ensure that the content does not violate any statutory provisions and that no rights of third parties are infringed by the content or its publication. Otherwise, liability of the parties shall be limited to intent and gross negligence.
7. You acknowledge and agree that the content is the copyrighted material of Micron and that the granting of permission by Micron to You as provided for herein constitutes the granting by Micron to You of a non-exclusive license to use the content strictly as provided for herein and shall in no way restrict or affect Micron’s rights in and/or to the content, including without limitation any publication or use of the content by Micron or others authorized by Micron.
8. Except for the above permission, Micron reserves all rights not expressly granted, including without limitation any and all patent and trade secret rights. Except as expressly provided herein, nothing herein will be deemed to grant, by implication, estoppel, or otherwise, a license under any of Micron’s other existing or future intellectual property rights.

How to cite sources from the Micron Educator Hub

- Micron is committed to collaborate with educators to make semiconductor memory education resources available through the Micron Educator Hub
- The content in the Micron Educator Hub has been identified by Micron as current and relevant to our company
- Please refer to the table on the right for proper citation

Use case	How to cite sources
a) Whole slide deck or whole document Description: User uses the whole slide deck or whole document AS IS, without any modification	No additional citation required
b) Full slide or full page Description: User incorporates a full slide or a full page into their own slide deck or document	“© 2025 Micron Technology, Inc. All Rights Reserved. Used with permission.”
c) Portion of a slide or portion of a page Description: User copies a portion of a slide or a portion of a page into a new slide or page	This is not allowed

How DRAM memory works - Quiz Ideas

- 1) What is the most common operation involving memory happening inside a computer when you load a program or video game?
 - A. Calculating mathematical equations
 - B. Copying data from SSD to DRAM
 - C. Rendering graphics
 - D. Sending data to the internet
- 2) How much faster is accessing data from DRAM compared to SSD?
 - A. 30 times faster
 - B. 300 times faster
 - C. 3000 times faster
 - D. 30,000 times faster
- 3) What is the capacity difference between a typical DRAM memory module (stick) and a solid-state drive (SSD)?
 - A. DRAM holds more data than SSD
 - B. SSD holds more data than DRAM
 - C. Both hold the same amount of data
 - D. DRAM holds data permanently, SSD does not
- 4) What is prefetching in the context of computer operations?
 - A. Moving data before it's needed
 - B. Deleting unnecessary files
 - C. Compressing data to save space
 - D. Encrypting data for security
- 5) Why do video games have DRAM capacity requirements?
 - A. To store game data permanently
 - B. To access game data quickly during gameplay
 - C. To store leaderboard information permanently
 - D. To reduce the size of the game files
- 6) What are the two main components of the DRAM memory cell?
 - A. Capacitor and resistor
 - B. Transistor and diode
 - C. Capacitor and transistor
 - D. Resistor and diode

How DRAM memory works - Quiz Ideas

- 7) What does a capacitor in a DRAM 1T1C memory cell store?
- A. One bit (binary digit) of data
 - B. Multiple bits (binary digits) of data
 - C. Resistance state
 - D. Magnetic fields
- 8) Why do DRAM capacitors need to be refreshed periodically?
- A. To increase storage capacity
 - B. To improve read/write speed
 - C. To reduce power consumption
 - D. To restore data due to charge leakage
- 9) What is the role of the sense amplifier in a DRAM cell?
- A. To store data
 - B. To isolate the capacitor
 - C. To connect the wordline and bitline
 - D. To amplify the voltage change on the bitline
- 10) True or False. In DRAM memory, rows, columns and banks are an addressing system used to select a specific set of bits to access.
- A. True
 - B. False
- 11) In the video, to ensure data is not lost, how often does the refresh operation occur for each bank in the DRAM mentioned?
- A. DRAM does not need refresh
 - B. Every 64 milliseconds
 - C. Every 1 second
 - D. Once a day
- 12) What is a row hit in DRAM? (Remember: row refers to a wordline)
- A. When a row is closed
 - B. When a row is damaged
 - C. When two rows are connected to each other
 - D. When a row is already open and can be accessed again

micron Educator Hub

micron

© 2025 Micron Technology, Inc. All rights reserved. Information, products, and/or specifications are subject to change without notice. All information is provided on an "AS IS" basis without warranties of any kind. Statements regarding products, including statements regarding product features, availability, functionality, or compatibility, are provided for informational purposes only and do not modify the warranty, if any, applicable to any product. Drawings may not be to scale. Micron, the Micron logo, and other Micron trademarks are the property of Micron Technology, Inc. All other trademarks are the property of their respective owners.